

LSSD-COMPATIBLE EDGE-TRIGGERED SHIFT REGISTERED LATCH

BUR920020128US1

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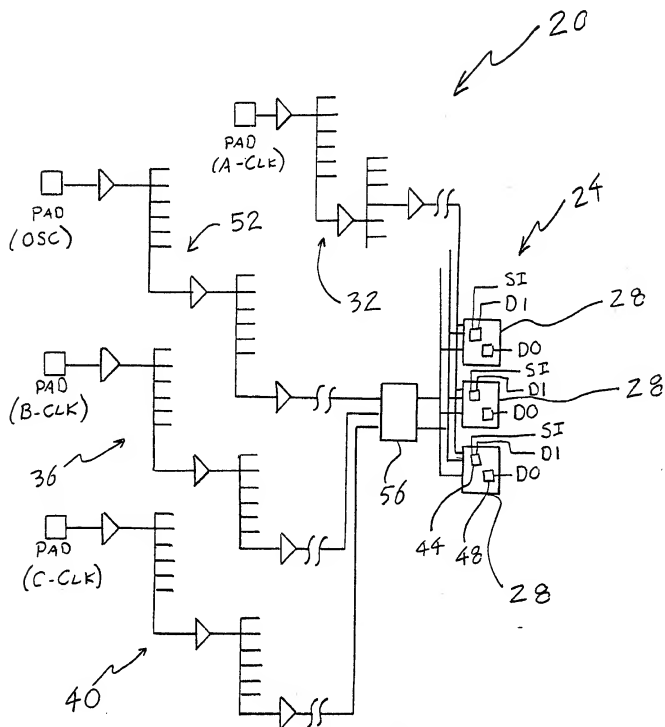


FIG 1
(PRIOR ART)

LSSD-COMPATIBLE EDGE-TRIGGERED SHIFT REGISTER LATCH
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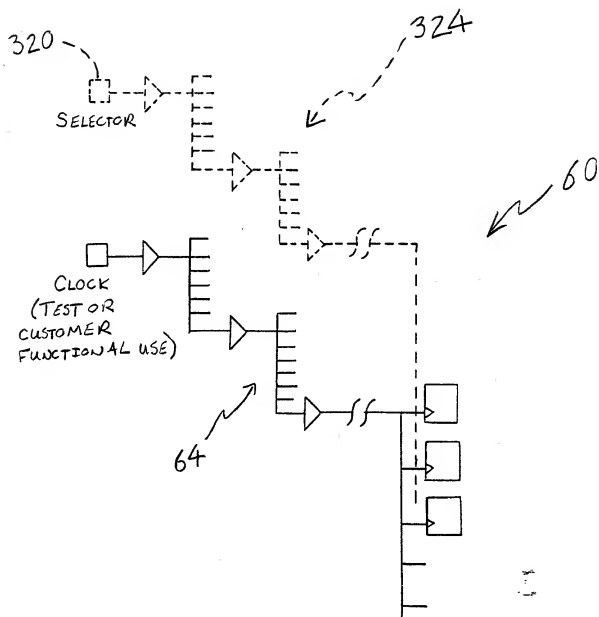


FIG. 2

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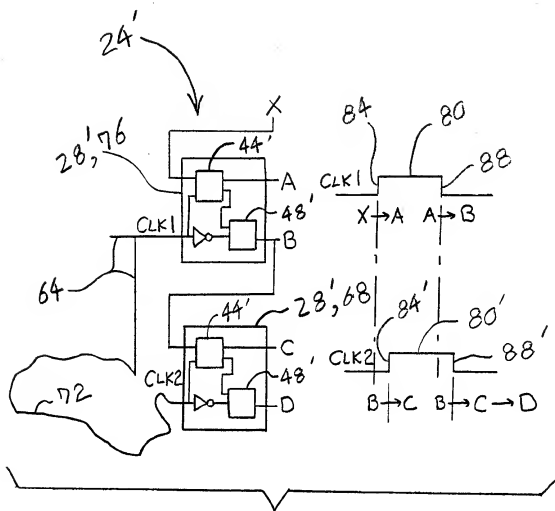
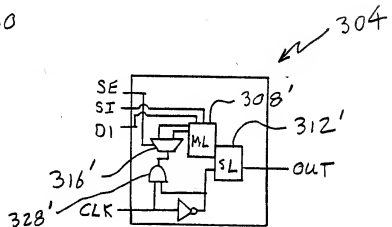
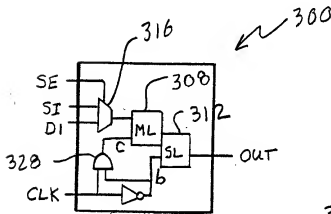
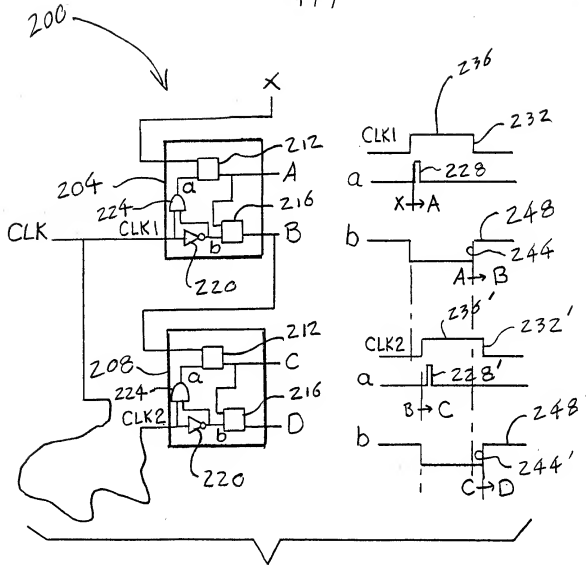
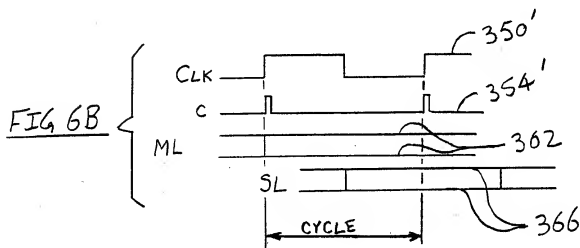
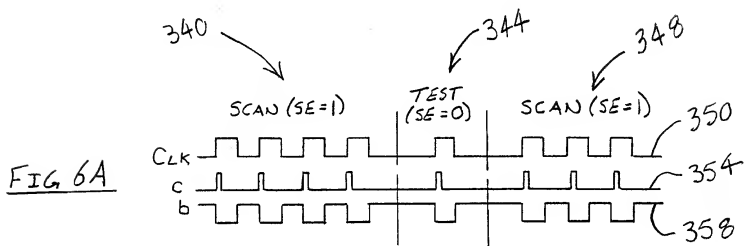
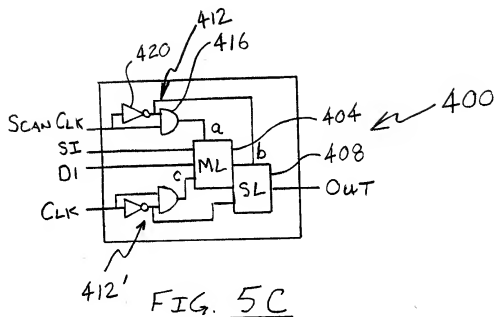


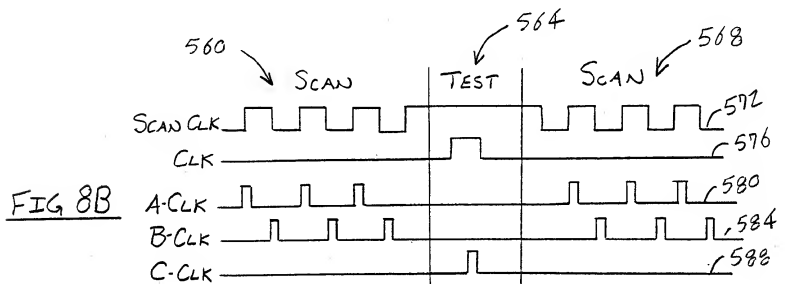
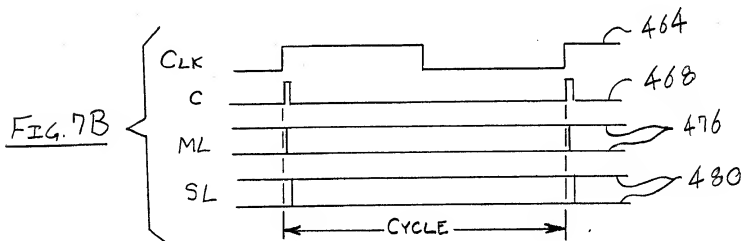
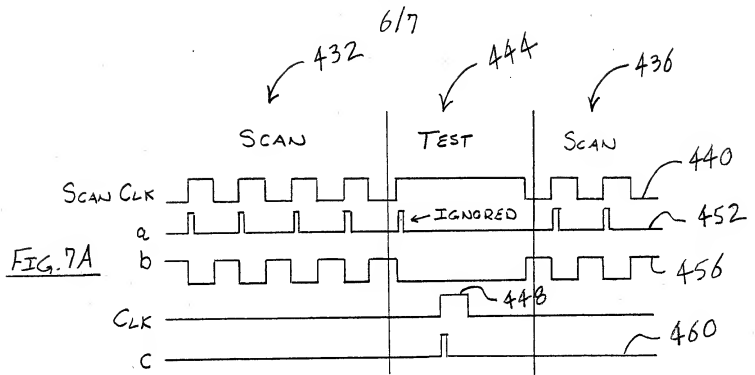
FIG. 3
(PRIOR ART)

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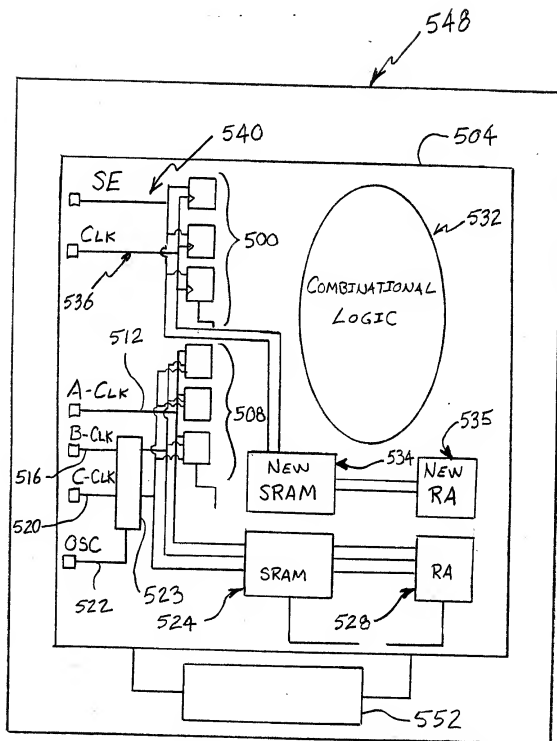


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FIG. 8A